1. The instruction, JML C008:2000h is example of🡺far jump/ intersegment jump

2. AX=? 🡺 -3val

3. Var3=? 🡺 1,2,7,2,1,3

4. The value of BX register follows the excution of MOV BX,[1D49] is F57F🡺little-endian

5. To clear one or more bít in a byte value, use\_\_\_\_\_instruction🡺AND

6. Write a logiccal shift instruction that divides EBX by 4🡺shr ebx,2

7. What will be the value of the destination operand after each of the following instructions execute in sequence🡺00000001h, 00001000h, 00000002h, FFFFFFFCh

8. Which of the following instructions are not legal addressing 🡺MOV AX, [BX+SP] / MOV AX, [SP+1]

9. Gien an assembly code copying the memory buffer Buff1 to Buff2. Choose equivalent string operations in place of block🡺CLD cp\_loop: REP MOVSB, LOOP cp\_loop

10. The result of an IMUL instruction is 0060, what is the corect state of Carry flag and Overflow flag 🡺 CF=0,OF=0

11. To set one or more bits in a byte value, use \_\_\_\_ instruction 🡺 OR

12. Choose the correct structure of memory chip as shown below 🡺 SRAM 2Kx8-bit

13. Sign-extend number 110101 (8-bit) to 16-bit🡺1111 1111 1111 0101

14. Implement the following pseudocode in assembly language🡺 cmp ebx,ecx ; ja L1; cmp ebx,val1; ja L1; mov X,2; jmp next; L1: mov X,1; next

15. To encrypt a byte value, use\_\_\_\_instruction🡺XOR

16. Convert the 8-bit floating point number d3 (in hex) to decimal 🡺-4.75

17. To isolate one or more bits in a byte value, use\_\_\_\_instruction🡺AND

18. The instruction, MOV AX, 0005h belongs to which addressing mode 🡺 Immediate

19. Given a code snippet. What is the equipvalent logic sequence of instruction in Assembly🡺 A

20. Convert the 8-bit floating point number E7 (in hex) to decimal 🡺-11.5

21. Convert the 32-bit floating point number A3358000 (in hex) to decimal 🡺 -9.83913471531x10^-18

22. The flag that the instruction SBB uses is 🡺carry flag

23. The instruction that supports addition when carry exists is 🡺ADC

24. Write instructions that jump to label L1 when the unsigned integer in DX is less than or equal to the integer in CX🡺 cmp dx,cx / jbe L1

25. In multiplication instruction, the result is taken from AX means the source operand is \_\_\_ is 🡺 8 bit

26. Convert the 8-bit floating point number D7 (in hex) to decimal 🡺-5.75

27. Which are corect about the Pointer resgisters of IA-32 processors🡺 ESP,EBP,EIP

28. In the RCR instruction, the contents of the destination operand undergoes function as 🡺carry flag, MSB,LSB, carry flag

29. To test one bit in a byte value without destructing the byte, use \_\_\_ instruction 🡺TEST

30. The instruction, MOV AX, [3004h] is an example of 🡺 dirrect addressing mode

31. Which could be corect ones for the source operand in an instruction🡺register, immediate data, memory location

32. The value of DX register follows the excution of MOV DX,[1D4D] is 127B🡺big-endian

33. In multiplication instruction, when the source operand is 8 bit,\_\_\_will be multiplied with source🡺AL

34. After excuting PUSH EAX instruction, the stack pointer 🡺decrements by 4

35. Convert the 32-bit floating point number C4361000 (in hex) to decimal 🡺-728.25

36. How many times the instruction below will loop 🡺1

37. The result of an IMUL instruction is FFA0, what is the corect state of Carry flag and Overflow flag 🡺CF=OF=1